

**AMENDMENTS TO THE CLAIMS**

**This listing of claims will replace all prior versions and listings of claims in the application:**

**LISTING OF CLAIMS:**

1. (original): A compound semiconductor light-emitting device having a light-emitting layer on a substrate, wherein at least a part of a substrate portion of the device side surface has recessed portions in a side direction of the device.
2. (original): A light-emitting device according to claim 1, wherein at least a part of a compound semiconductor portion of the device side surface has recessed portions in a side direction of the device.
3. (currently amended): A light-emitting device according to claim 1-~~or 2~~, wherein the light-emitting layer comprises an n-type or p-type compound semiconductor and is of the pn junction type.
4. (currently amended): A light-emitting device according to ~~any one of claims 1 to 3~~claim 1, wherein the substrate is selected from the group consisting of a sapphire, a SiC and a III-V Group compound semiconductor single crystal.
5. (currently amended): A light-emitting device according to ~~any one of claims 1 to 4~~claim 1, wherein recessed portions are existing maintaining a distance of 4 to 40  $\mu\text{m}$ .
6. (currently amended): A light-emitting device according to ~~any one of claims 1 to 5~~claim 1, wherein the recessed portions have a depth of 0.5 to 10  $\mu\text{m}$  and a width of 1 to 20  $\mu\text{m}$ .
7. (currently amended): A light-emitting device according to ~~any one of claims 1 to 6~~claim 1, wherein the compound semiconductor light-emitting device is of the flip-chip type.

8. (original): A method of producing compound semiconductor light-emitting device comprising the steps of:

- (a) forming a compound semiconductor layer including a light-emitting layer of an n-type or p-type compound semiconductor on a wafer that serves as a substrate,
- (b) arranging a negative electrode and a positive electrode at predetermined positions for passing a drive current through the light-emitting layer,
- (c) forming a separation zone for separating the individual light-emitting devices,
- (d) perforating many small holes linearly in the wafer that serves as the substrate along the separation zone, and
- (e) dividing the wafer into individual light-emitting devices along the separation zone.

9. (original): A method of production according to claim 8, wherein the small holes are formed by a laser method.

10. (currently amended): A method of production according to claim 8-~~or 9~~, wherein the opening diameter of the small holes is 1 to 20  $\mu\text{m}$ .

11. (currently amended): A method of production according to ~~any one of claims 8 to 10~~ claim 8, wherein the depth of the small holes is 1/10 to 3/4 of the thickness of the substrate.

12. (currently amended): A method of production according to ~~any one of claims 8 to 11~~ claim 8, wherein the distance between the small holes is 4 to 40  $\mu\text{m}$ .

13. (currently amended): A method of production according to ~~any one of claims 8 to 12~~<sup>claim 8</sup>, wherein the small holes are perforated from the side of the compound semiconductor layer formed on the front surface of the substrate.

14. (currently amended): A method of production according to ~~any one of claims 8 to 13~~<sup>claim 8</sup>, wherein the small holes are perforated from the back surface of the substrate.

15. (original): A method of production according to claim 14, wherein the depth of the small holes in the back surface of the substrate is deeper than that of the small holes in the front surface of the substrate.

16. (currently amended): A method of production according to ~~any one of claims 8 to 15~~<sup>claim 8</sup>, wherein the small holes are periodically perforated.

17. (original): A method of producing compound semiconductor light-emitting device comprising the steps of:

- (a) forming a compound semiconductor layer including a light-emitting layer of an n-type or p-type compound semiconductor on a wafer that serves as a substrate,
- (b) arranging a negative electrode and a positive electrode at predetermined positions for flowing a drive current to the light-emitting layer,
- (c) forming a separation zone for separation into the individual light-emitting devices,
- (d) projecting intermittently a laser beam setting a focal position in the wafer serving as the substrates to linearly scan along the separation zone, and
- (e) dividing the wafer into individual light-emitting devices along the separation zone.

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18. (currently amended): A lamp comprising a compound semiconductor light-emitting device of ~~any one of claims 1 to 7~~claim 1.

19. (original): A source of light comprising a lamp of claim 18.